



FIG. 1

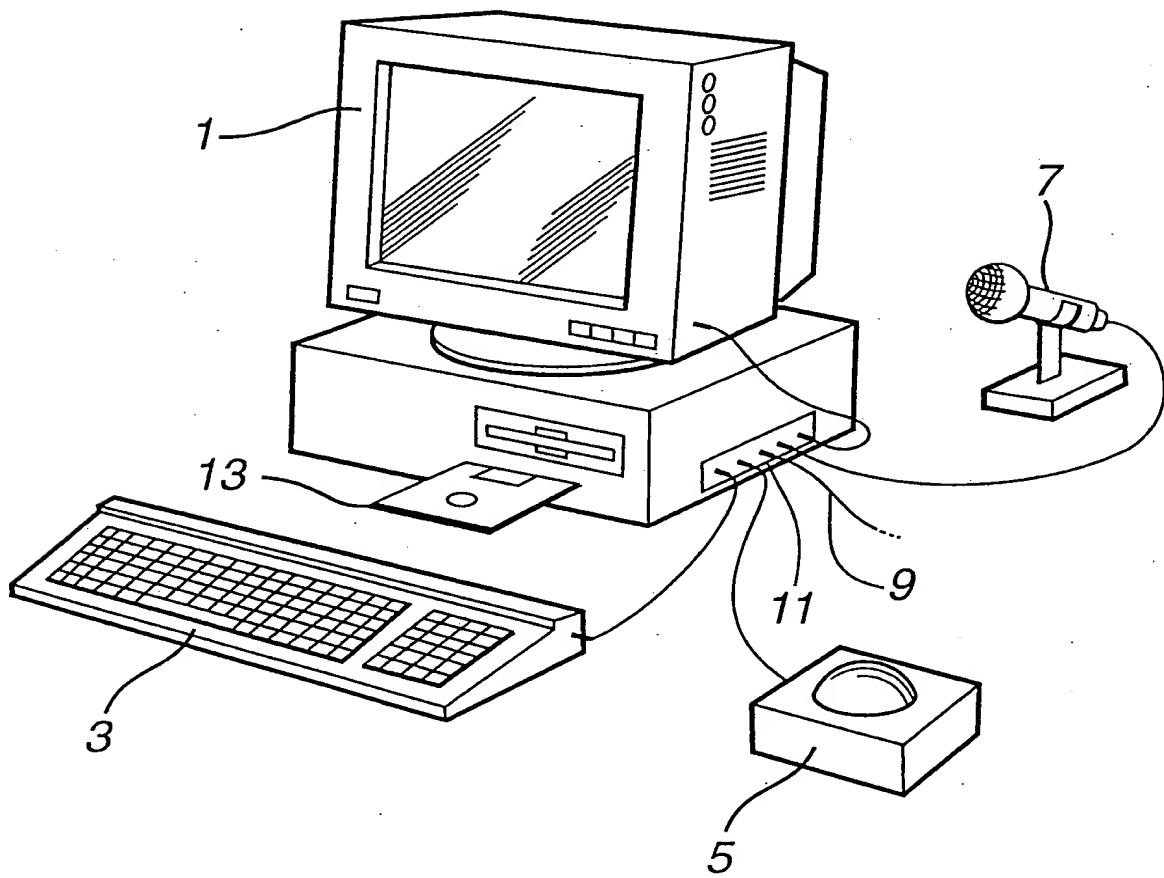


FIG. 2

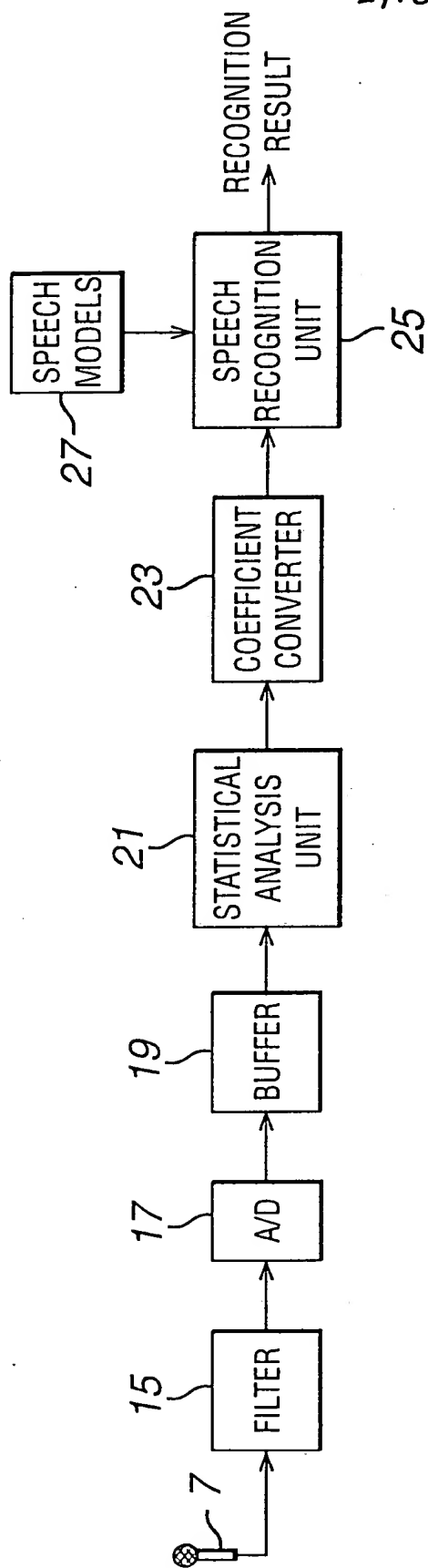
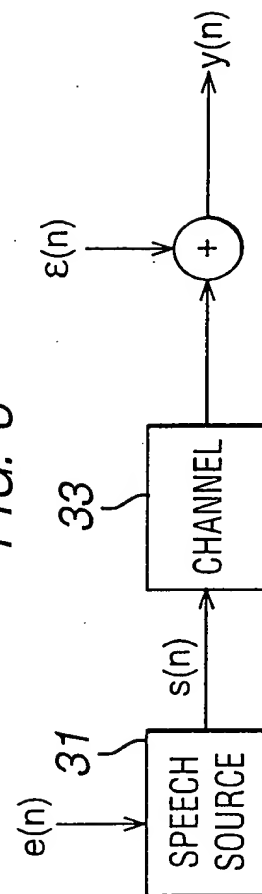
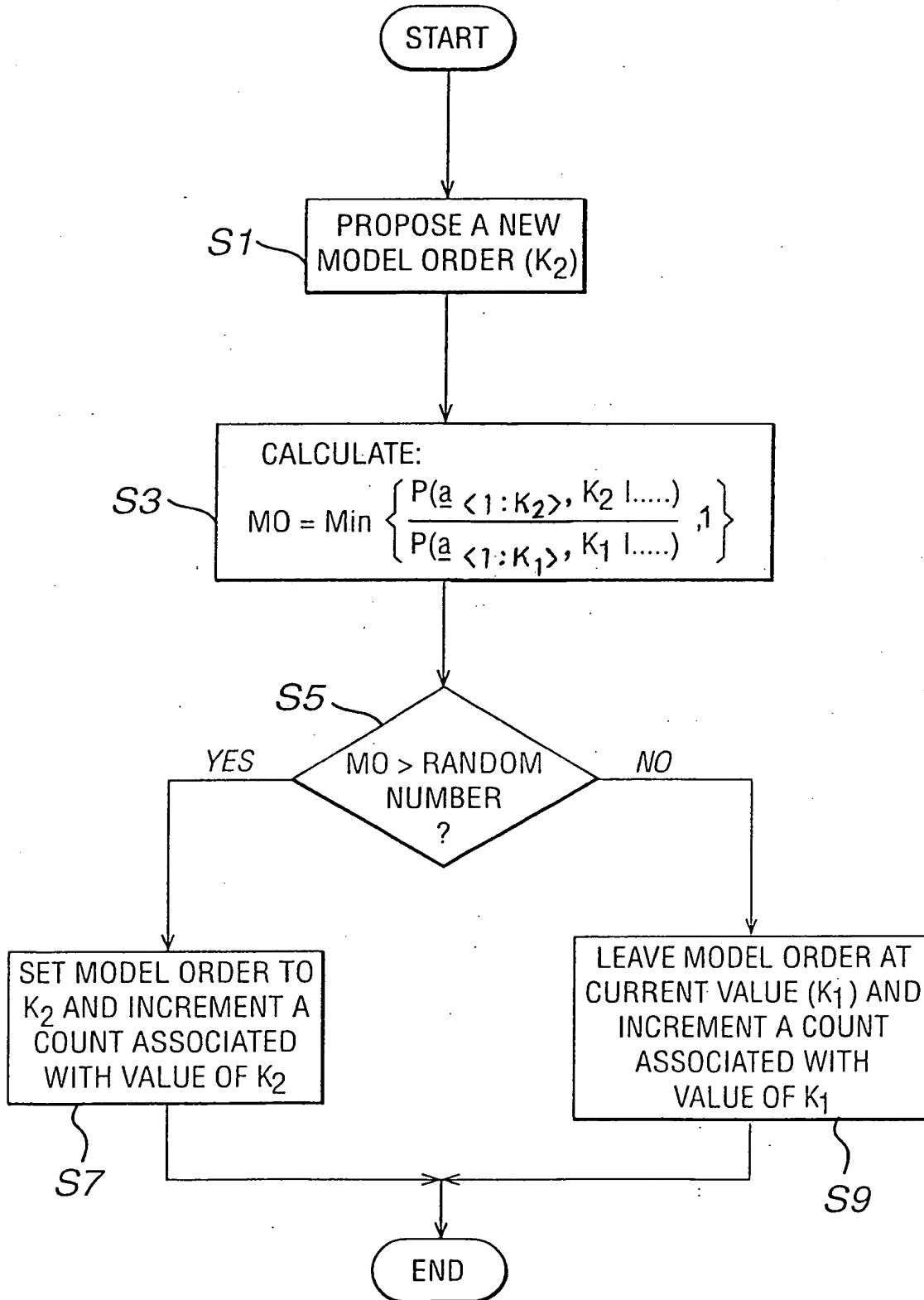


FIG. 3



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FIG. 4



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FIG. 5

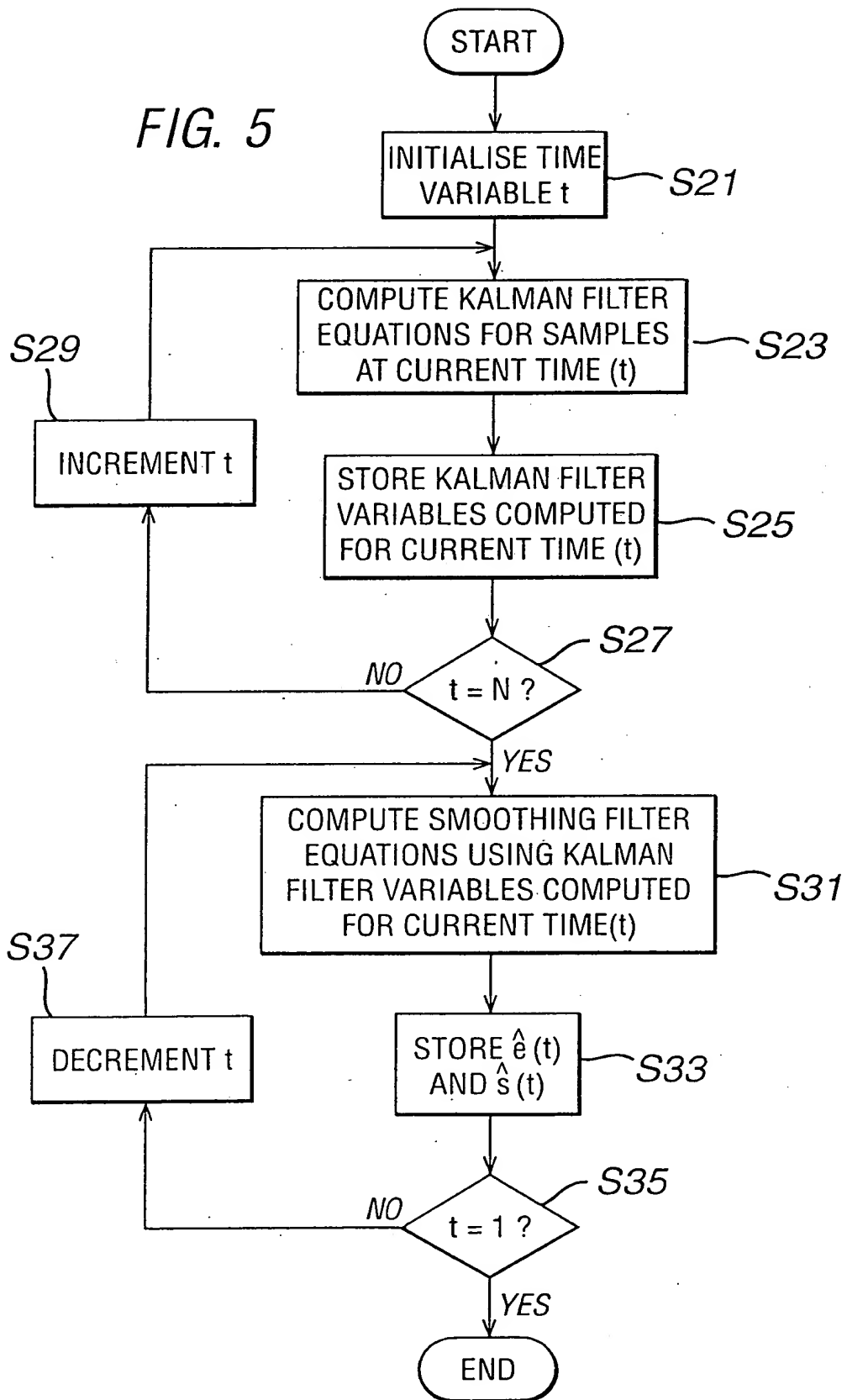
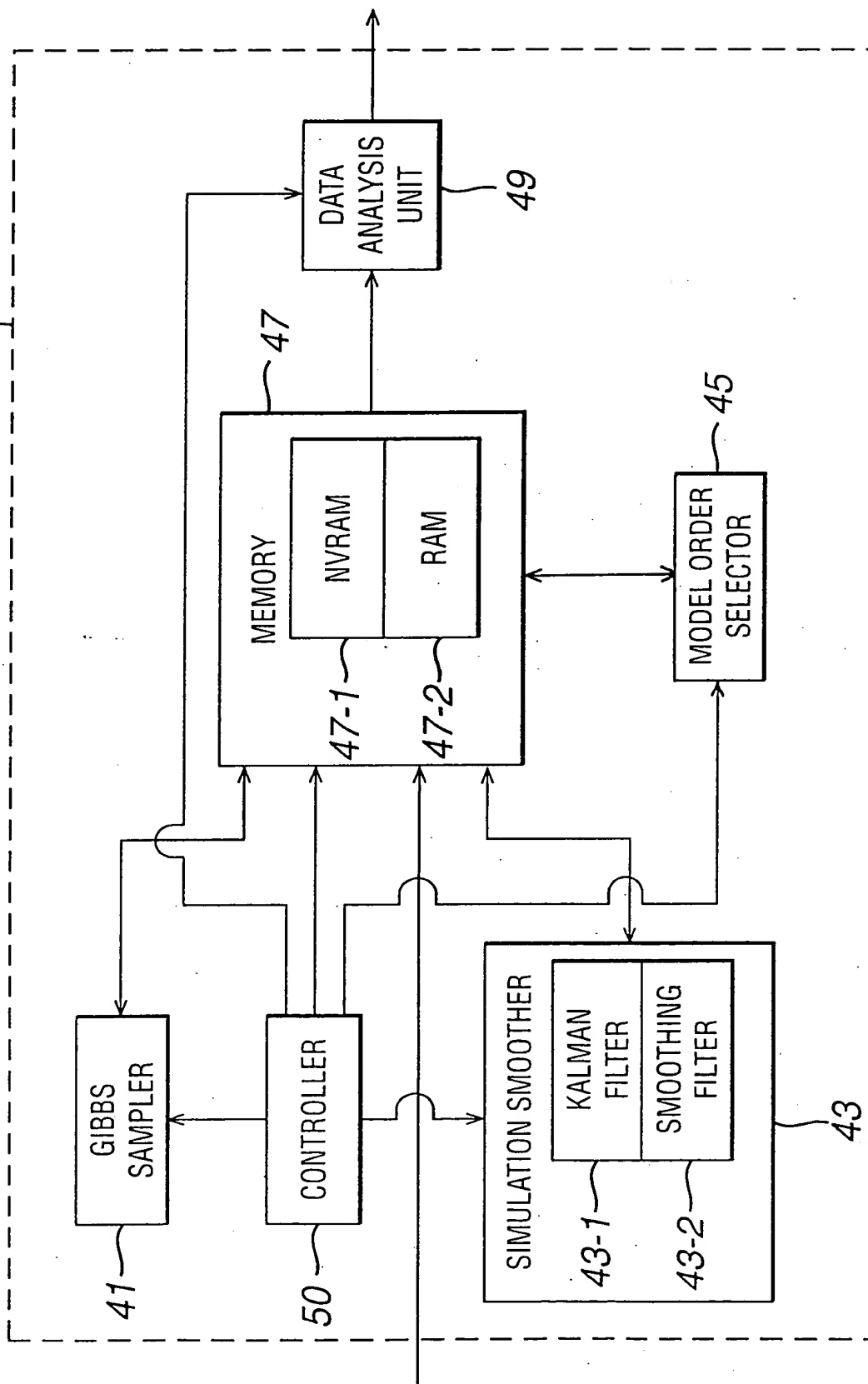


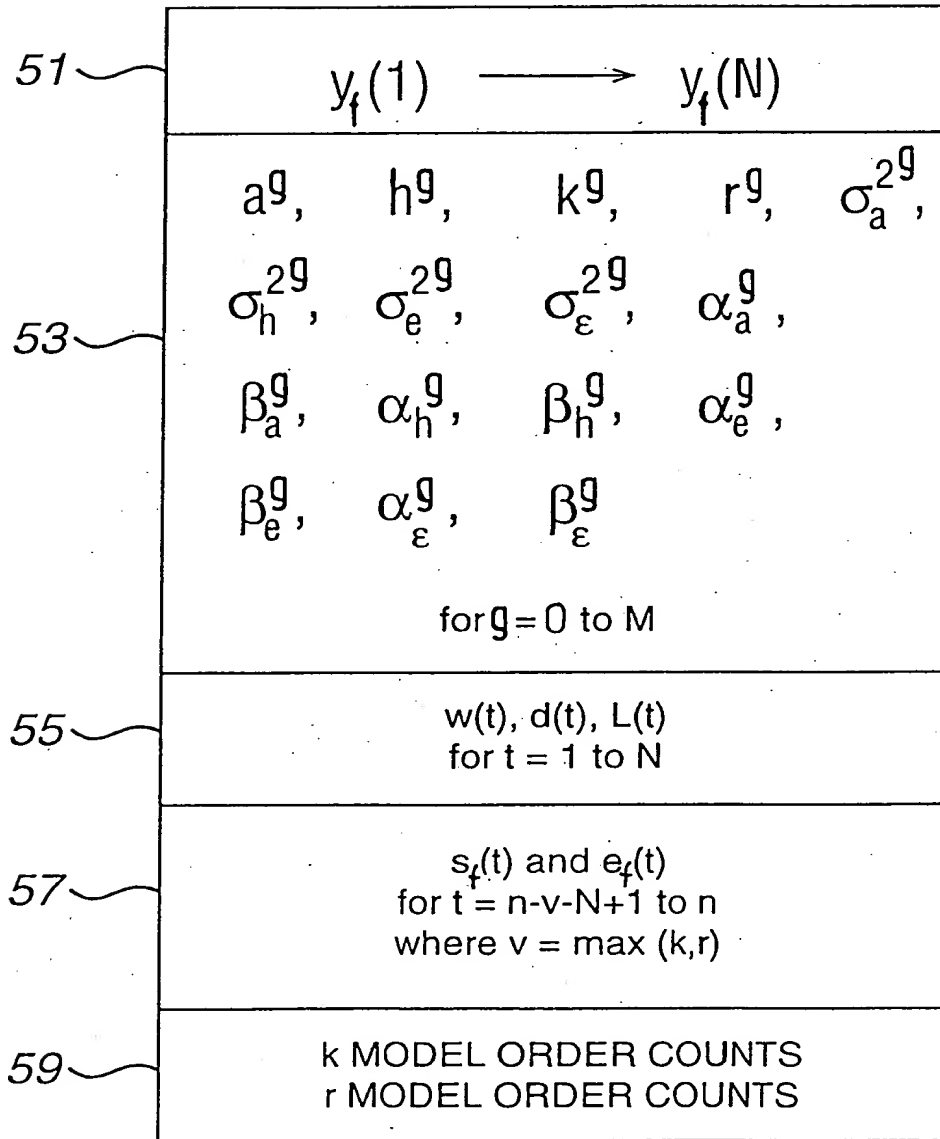
FIG. 6

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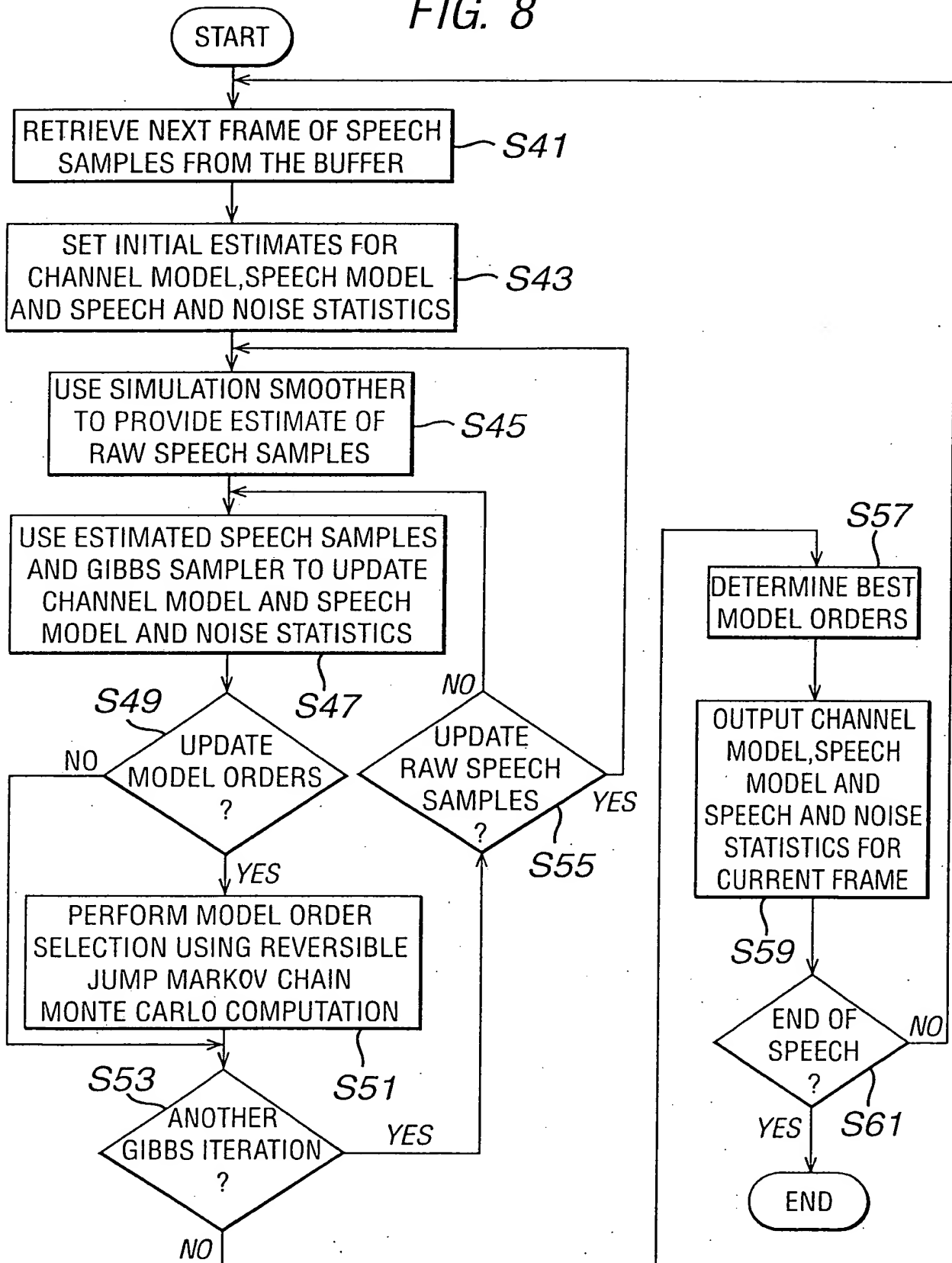
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FIG. 7



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FIG. 8





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FIG. 9a

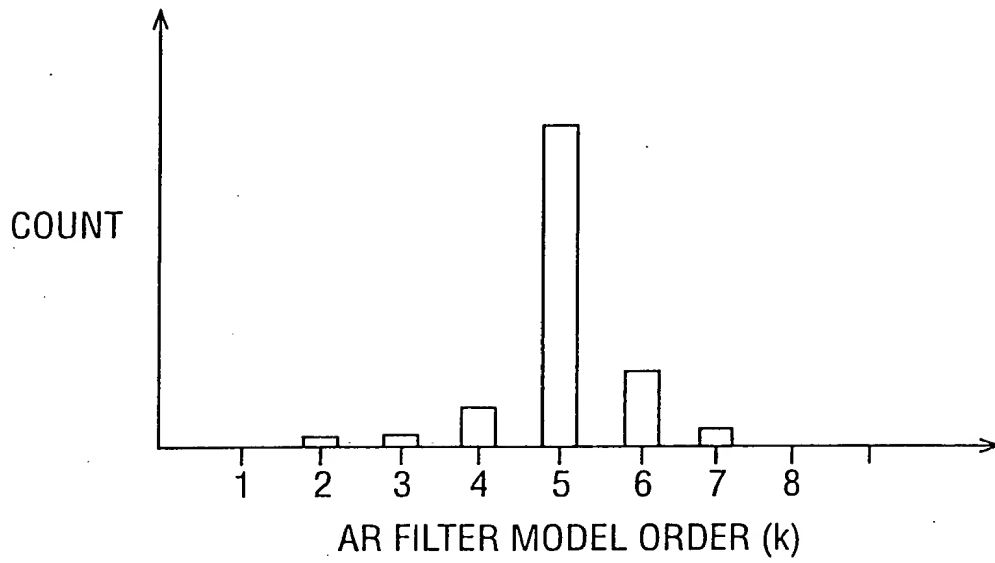
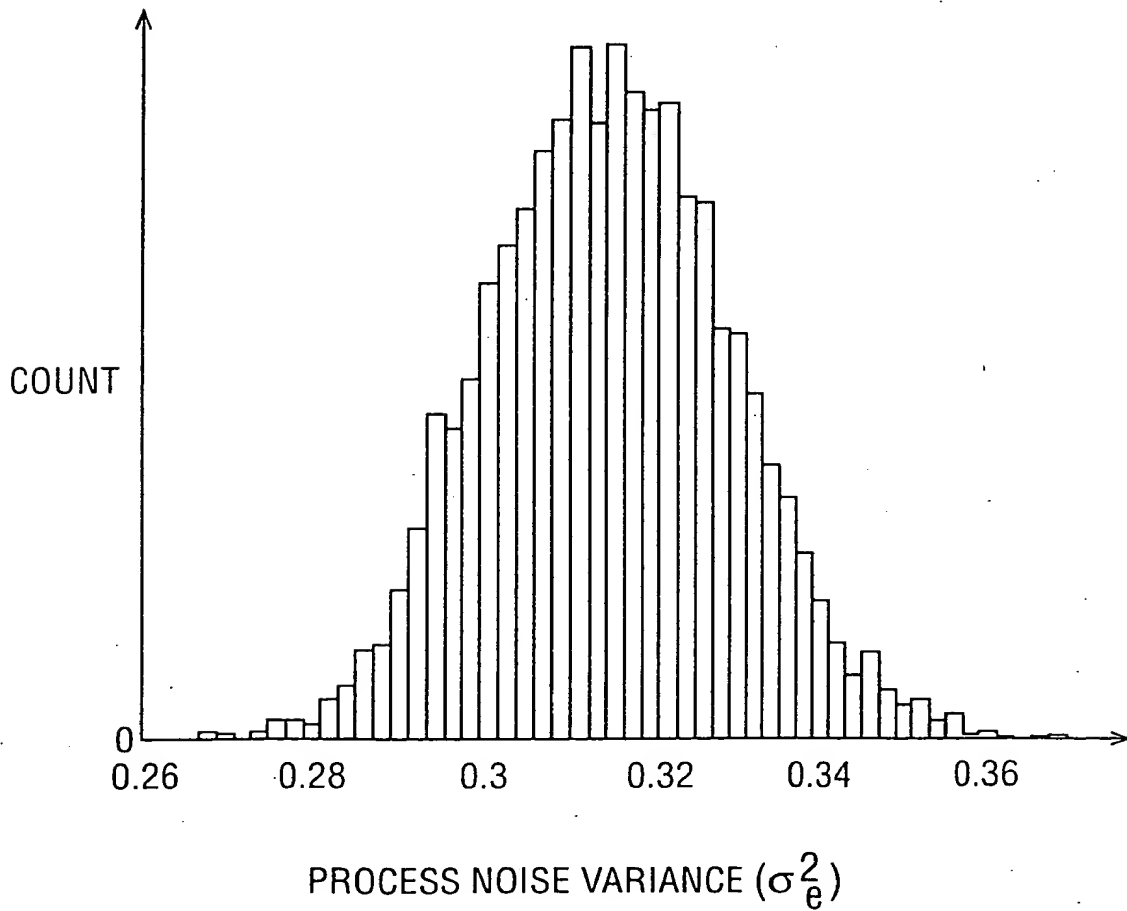
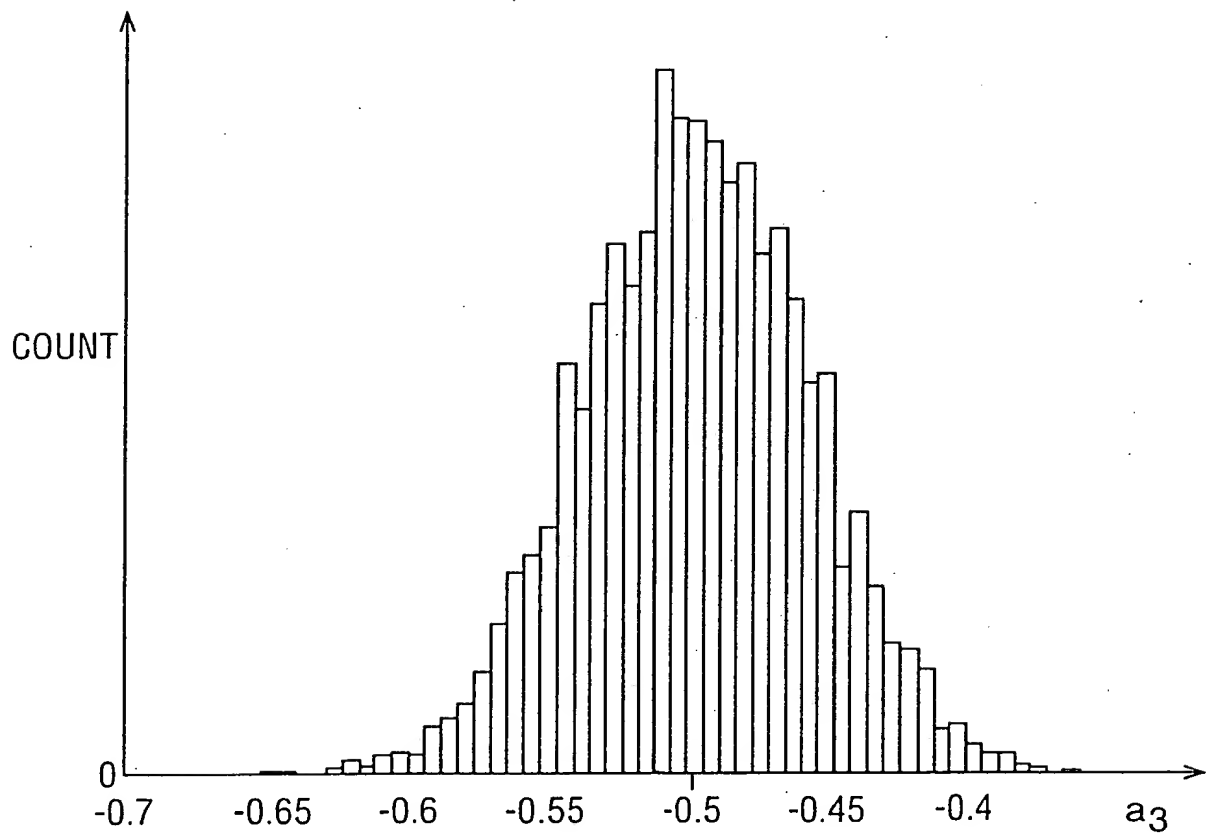


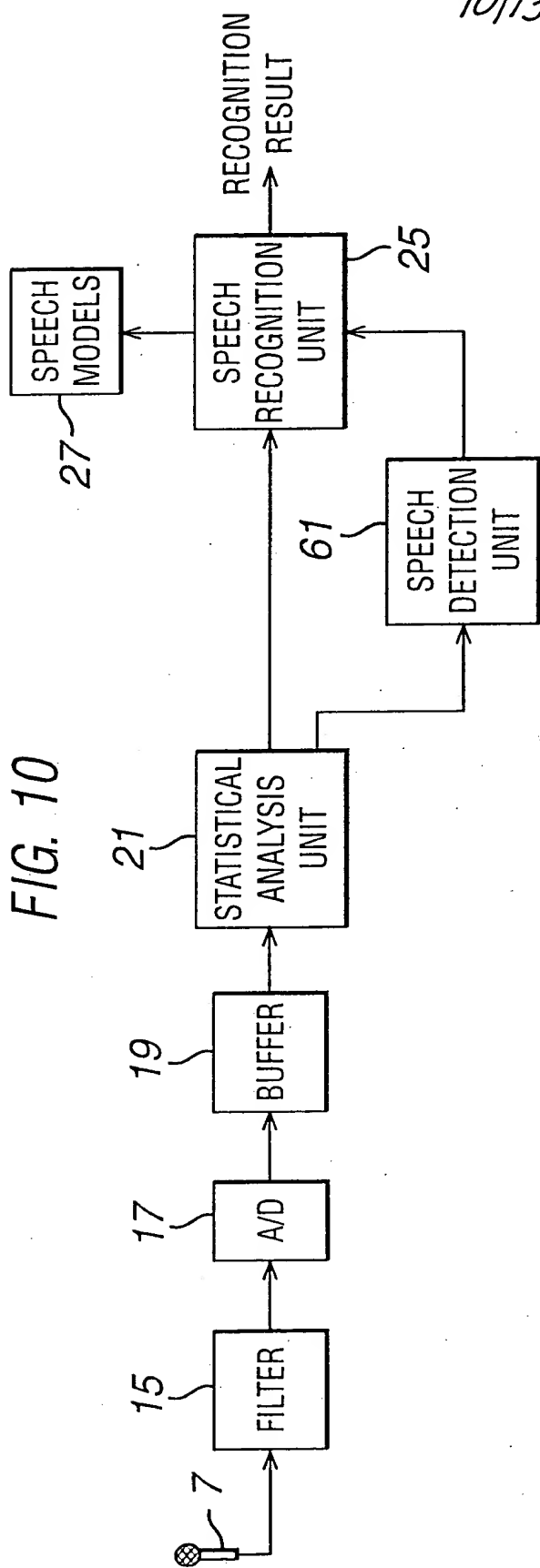
FIG. 9b



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FIG. 9c





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FIG. 11

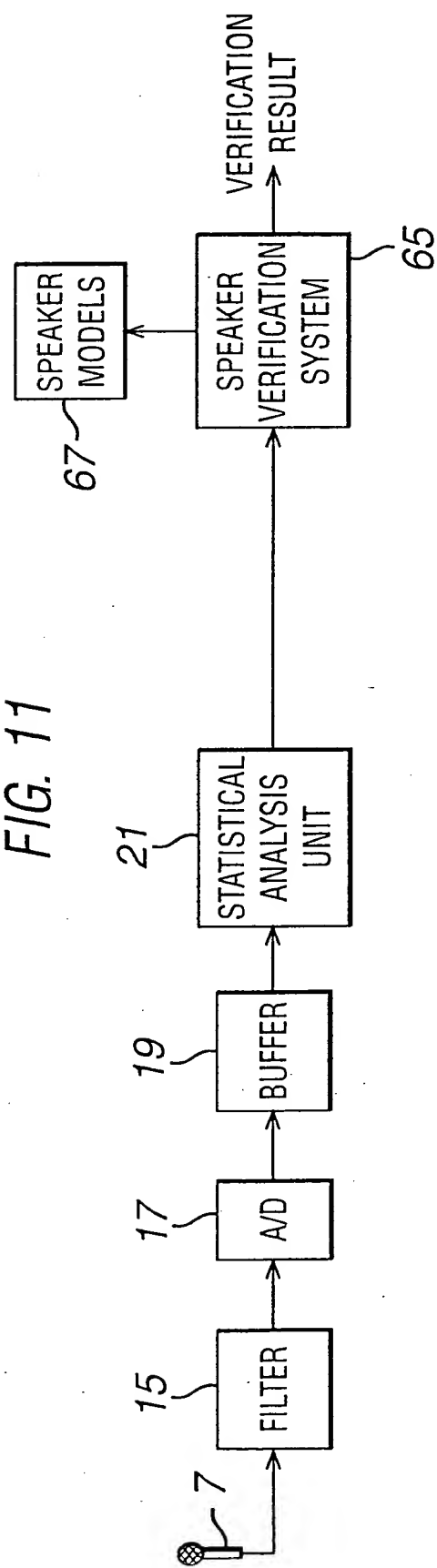
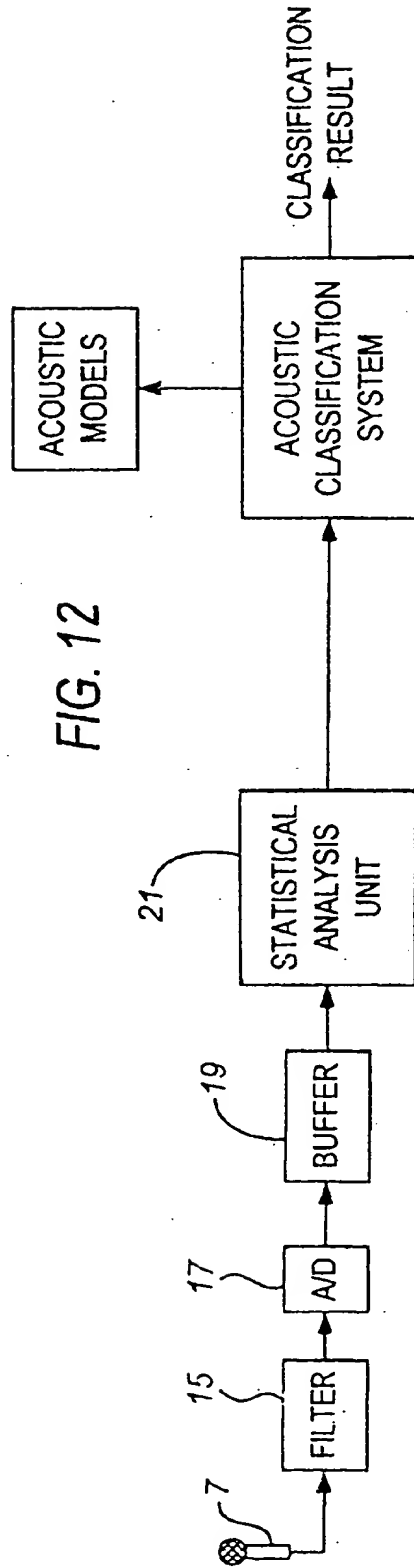


FIG. 12



The diagram illustrates a speech recognition and synthesis system. On the left, the transmitter side consists of a microphone (7) connected to a FILTER (15), an A/D converter (17), a BUFFER (19), a STATISTICAL ANALYSIS UNIT (21), a CHANNEL ENCODER (71), and a TRANSMITTER (73). On the right, the receiver side includes a RECEIVER (75), a DECODER (76), and a dashed box (83) containing a SPEECH RECOGNITION UNIT (77) and a SPEECH SYNTHESIS UNIT (79). A speaker (81) is connected to the synthesis unit. A lightning bolt symbol indicates wireless communication between the transmitter and receiver. Arrows show the flow of data and the output of the recognition unit.

FIG. 14

